

Fig. 4

OUTPUTTING $0 < Dv1 < Th1 \Rightarrow k0$

OUTPUTTING $Th1 \leq Dv1 < Th2 \Rightarrow k1$

OUTPUTTING $Th2 \leq Dv1 < Th3 \Rightarrow k2$

Th1: FIRST THRESHOLD VALUE

Th2: SECOND THRESHOLD VALUE

Th3: THIRD THRESHOLD VALUE

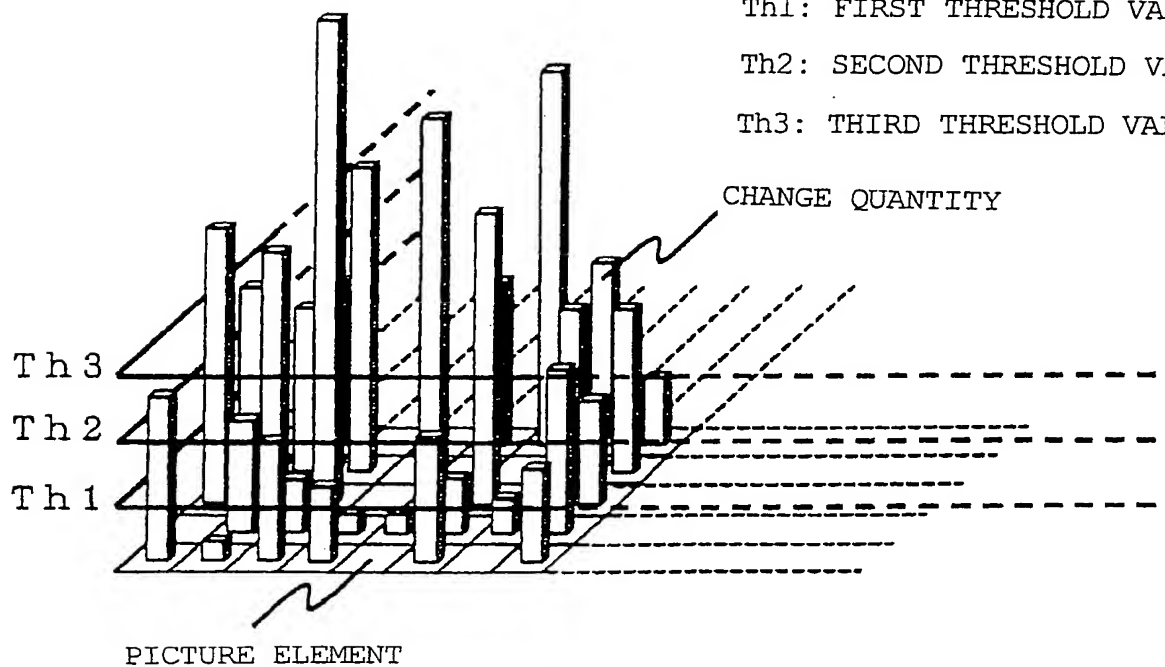


Fig. 10

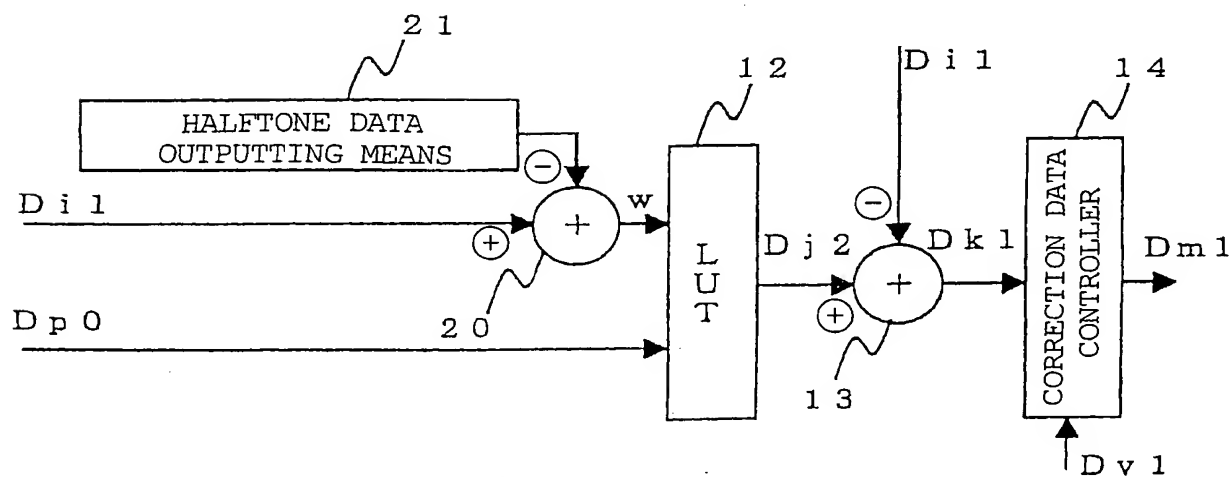


Fig. 11

Inventor: Noritaka OKUDA et al.

Title: CORRECTION DATA OUTPUT DEVICE, FRAME DATA
CORRECTION DEVICE, FRAME DATA DISPLAY, etc.
REPLACEMENT SHEET

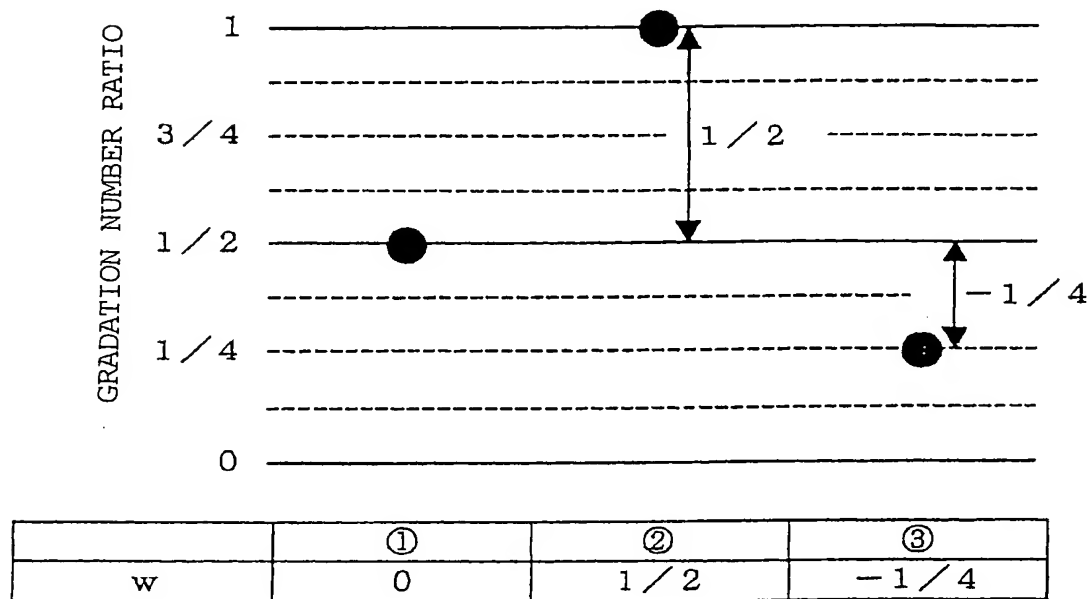


Fig. 12

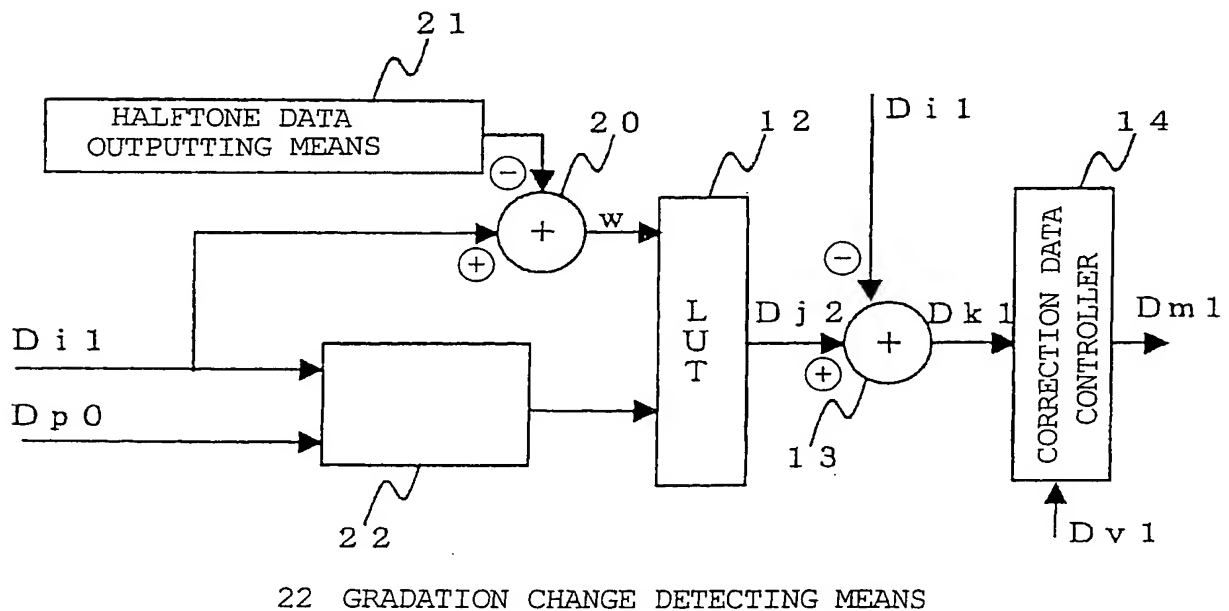


Fig. 13

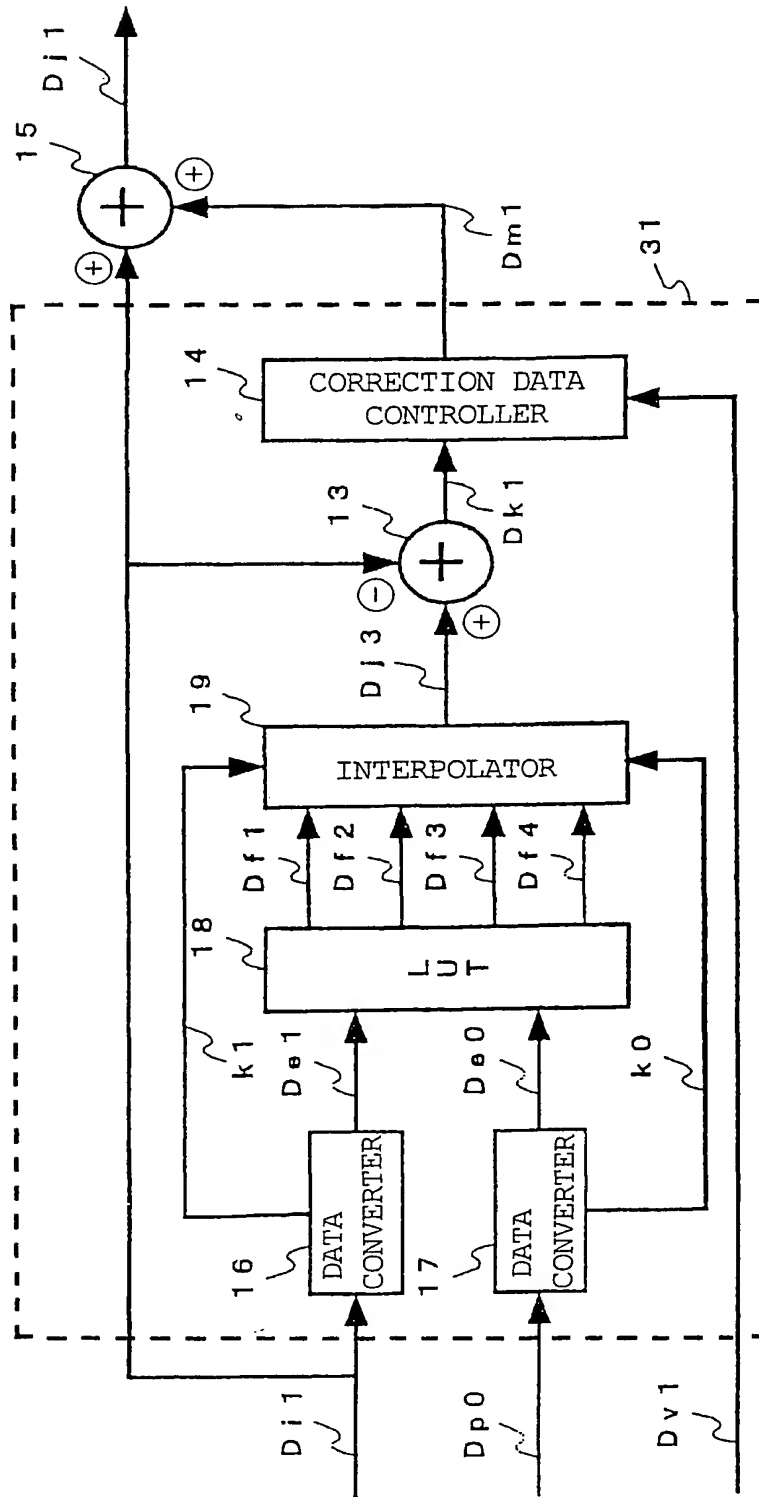


Fig. 17

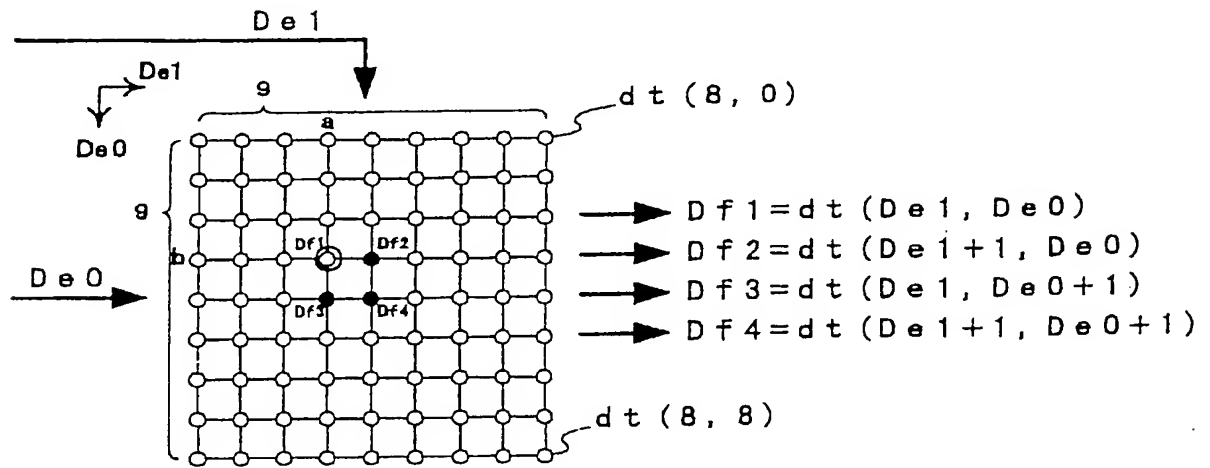


Fig. 18

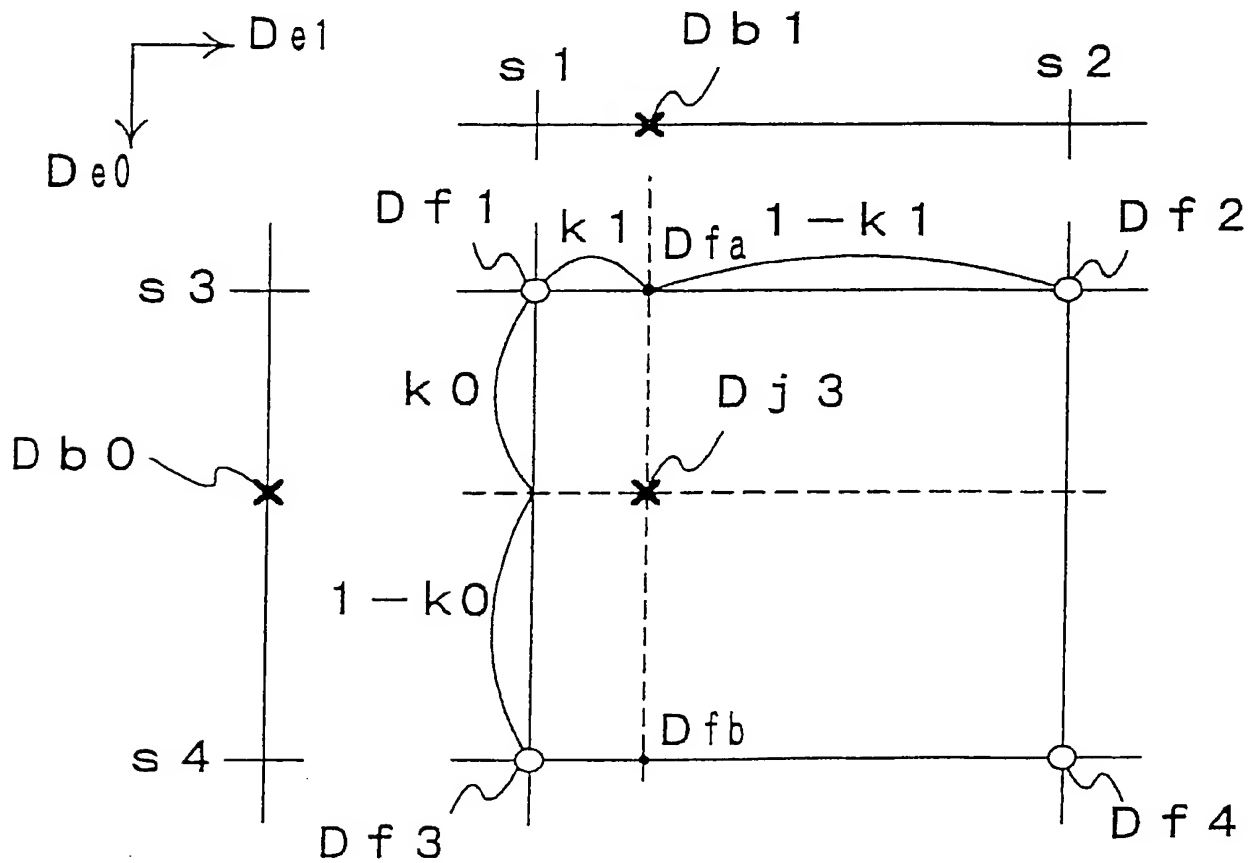


Fig. 19